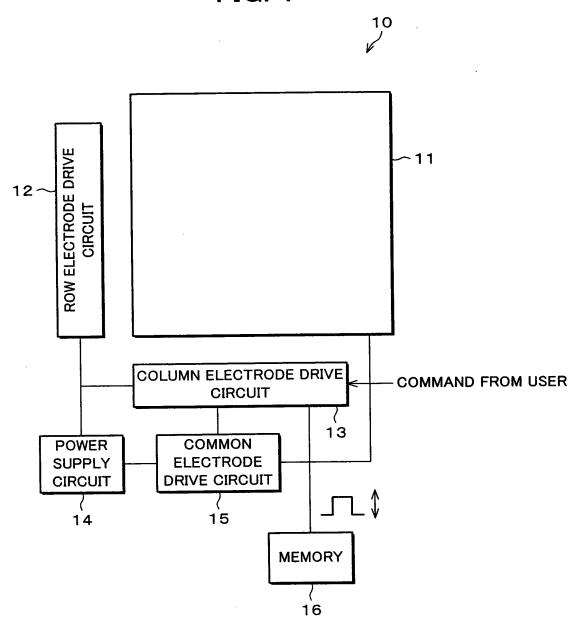
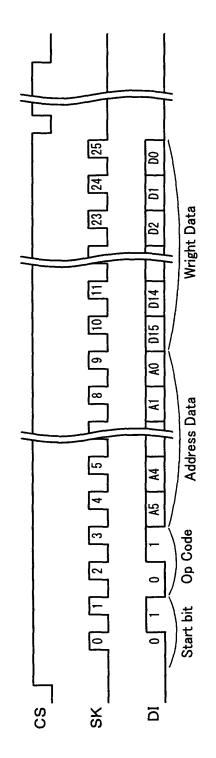
FIG. 1



00000000 \0 5V 11111111 FIG. 2 (b) WRITING READOUT Vsopt1 Vsopt2 SOURCE ELECTRODE DRIVE CIRCUIT ADDRESS 1 (8bit) ADDRESS 2 (8bit) FIG. 2 (a) DATA LINE (DI) -DATA LINE (DO) -CLOCK (SCK) -MEMORY CHIP SELECT (CS) —





WRITE



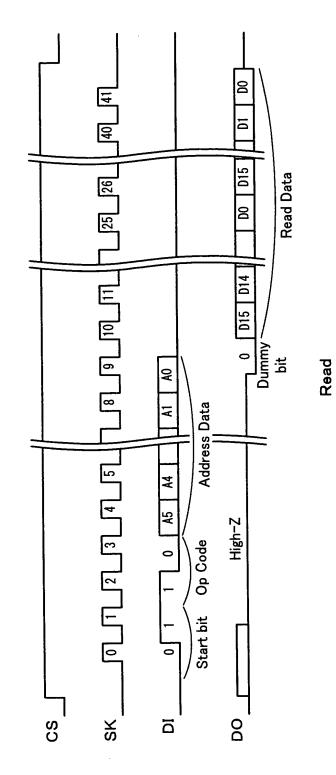
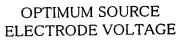
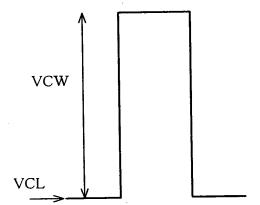


FIG. 5





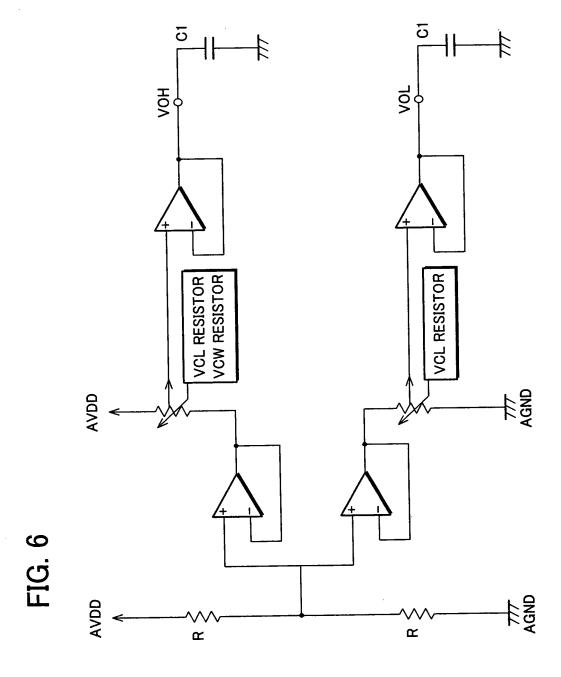


FIG. 7

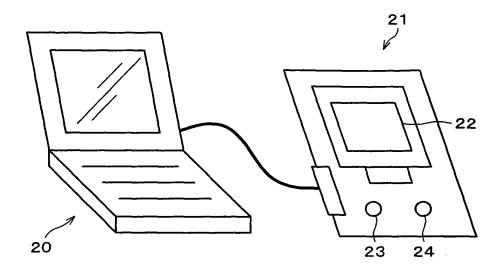


FIG. 8

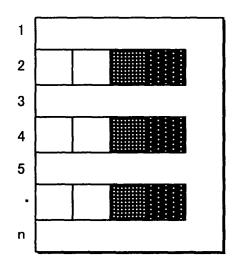


FIG. 9 (a)

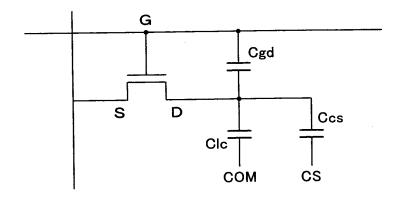


FIG. 9 (b)

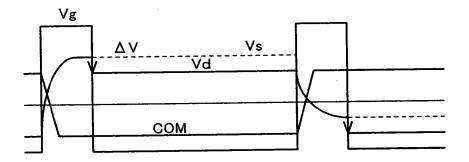


FIG. 10

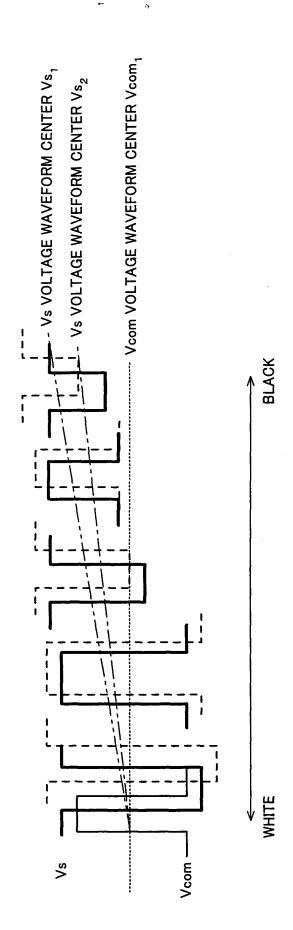


FIG. 11

